

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S45	17	717/135.ccls. (dsp or (fpga vhd))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/09/16 10:53
S44	24	fpga (function adj2 block) ((((\$3program\$4 or \$3configur\$6) near8 fpga) ((((\$3program\$4 or \$3configur\$6) with (function adj2 block)) simulat\$4 dsp numeric\$2	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/09/16 10:25
S43	3	fpga (function adj2 block) ((((\$3program\$4 or \$3configur\$6) near8 fpga) same (function adj2 block)) simulat\$4 dsp numeric\$2	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/09/16 10:21
S42	14	fpga (function adj2 block) ((((\$3program\$4 or \$3configur\$6) near8 fpga) same function same block) simulat\$4 dsp numeric\$2	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/09/16 10:20
S41	36	fpga (function adj2 block) ((((\$3program\$4 or \$3configur\$6) near8 fpga) same function same block) simulat\$4 dsp	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/09/16 10:18
S40	77	fpga (function adj2 block) ((((\$3program\$4 or \$3configur\$6) near8 fpga) same function same block) simulat\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/09/16 10:18
S39	264	fpga (function adj2 block) ((((\$3program\$4 or \$3configur\$6) near8 fpga) same function same block)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/09/16 10:18
S38	1040	fpga (function near2 block) ((\$3program\$4 or \$3configur\$6) near8 fpga)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/09/16 10:17
S1	29319	((computing or computation\$2 or numeric\$2) same simulat\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/09/16 10:16

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S37	36	((parallel or distribut\$4 or partition\$4) near4 (comput\$3 or computation\$2 or simulat\$4)) (numeric\$2 same (comput\$3 or computation\$3 or simulat\$4)) simulat\$4 vhdl fpga dsp	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/09/15 10:22
S36	37	((parallel or distribut\$4 or partition\$4) near4 (comput\$3 or computation\$2 or simulat\$4)) (numeric\$2 same (comput\$3 or computation\$3 or simulat\$4)) vhdl fpga dsp	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/09/15 10:22
S35	9020	((parallel or distribut\$4 or partition\$4) near4 (comput\$3 or computation\$2 or simulat\$4)) (numeric\$2 same (comput\$3 or computation\$3 or simulat\$4))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/09/15 10:21
S34	35	vhdl (fpga or dsp) fpga ((parallel or distribut\$4 or partition\$4) near3 (comput\$3 or computation\$2 or simulat\$4)) simulat\$4 (numeric\$2 same (comput\$3 or computation\$2 or simulat\$3)) ((parallel or distribut\$4 or partition\$4).ti. or (parallel or distribut\$4 or partition\$4).clm.)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/09/15 10:20
S33	55	vhdl (fpga or dsp) fpga ((parallel or distribut\$4 or partition\$4) near3 (comput\$3 or computation\$2 or simulat\$4)) simulat\$4 numeric\$2 ((parallel or distribut\$4 or partition\$4).ti. or (parallel or distribut\$4 or partition\$4).clm.)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/09/15 10:09
S32	174	vhdl (fpga or dsp) fpga ((parallel or distribut\$4 or partition\$4) near3 (comput\$3 or computation\$2 or simulat\$4)) simulat\$4 ((parallel or distribut\$4 or partition\$4).ti. or (parallel or distribut\$4 or partition\$4).clm.)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/09/15 10:08
S31	182	vhdl (fpga or dsp) ((parallel or distribut\$4 or partition\$4) near3 (comput\$3 or computation\$2 or simulat\$4)) simulat\$4 ((parallel or distribut\$4 or partition\$4).ti. or (parallel or distribut\$4 or partition\$4).clm.)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/09/15 10:06
S30	525	vhdl (fpga or dsp) ((parallel or distribut\$4 or partition\$4) near3 (comput\$3 or computation\$2 or simulat\$4)) simulat\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/09/15 10:06
S29	661	vhdl (fpga or dsp) ((parallel or distribut\$4 or partition\$4) near3 (comput\$3 or computation\$2 or simulat\$4))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/09/15 10:05

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S28	61	(nallatech\$2.as. or minulink\$3.as. or xilinx.as.) fpga vhdl (numeric\$2 or simulat\$4) ((parallel or distribut\$4 or partition\$3))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/09/14 07:05
S27	84	(nallatech\$2.as. or minulink\$3.as. or xilinx.as.) fpga vhdl (numeric\$2 or simulat\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/09/14 07:05
S23	80	(nallatech\$2.as. or minulink\$3.as. or xilinx.as.) fpga vhdl simulat\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/09/14 07:04
S22	119	(nallatech\$2.as. or minulink\$3.as. or xilinx.as.) fpga vhdl	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/09/14 07:04
S16	19	(simulat\$4 near8 numeric\$2) vhdl (dsp or fpga) ((partition\$4 or distribut\$6 or parallel) with (comput\$3 or computation\$2 or simulat\$4 or rout\$4 or direct\$4)) (partition\$4 or distribut\$6 or parallel).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/09/14 06:59
S14	28	(simulat\$4 near8 numeric\$2) vhdl (dsp or fpga) ((partition\$4 or distribut\$6 or parallel) same (rout\$4 or direct\$4))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/09/14 06:48
S13	43	(simulat\$4 near8 numeric\$2) vhdl (dsp or fpga) (partition\$4 or distribut\$6 or parallel) (rout\$4 or direct\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/09/14 06:45
S12	45	(simulat\$4 near8 numeric\$2) vhdl (dsp or fpga) (partition\$4 or distribut\$6 or parallel)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/09/14 06:45
S10	26	(simulat\$4 near8 numeric\$2) (bit near2 file)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/09/14 06:44

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S9	44	(numeric\$2 near6 (computing or computation\$2 or simulat\$4)) (programmable or fpga) vhdl (processor or cpu or dsp) ((parallel or partition\$4 or distribut\$6) with (numeric\$2 or comput\$3 or computation\$2 or simulat\$4)) simulat\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/09/14 06:39
S8	44	(numeric\$2 near6 (computing or computation\$2 or simulat\$4)) (programmable or fpga) vhdl (processor or cpu) ((parallel or partition\$4 or distribut\$6) with (numeric\$2 or comput\$3 or computation\$2 or simulat\$4))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/09/13 18:22
S7	58	(numeric\$2 near6 (computing or computation\$2 or simulat\$4)) (programmable or fpga) vhdl (processor or cpu) (parallel or partition\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/09/13 17:50
S6	67	(numeric\$2 near6 (computing or computation\$2 or simulat\$4)) (programmable or fpga) vhdl (processor or cpu)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/09/13 17:50
S5	69	(numeric\$2 near6 (computing or computation\$2 or simulat\$4)) (programmable or fpga) vhdl	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/09/13 17:49
S4	45	((computing or computation\$2 or numeric\$2) near6 simulat\$4) (numeric\$2 near6 (computing or computation\$2 or simulat\$4)) (programmable or fpga) vhdl	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/09/13 17:48
S3	7364	((computing or computation\$2 or numeric\$2) near6 simulat\$4) (numeric\$2 near6 (computing or computation\$2 or simulat\$4))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/09/13 17:46
S2	13731	((computing or computation\$2 or numeric\$2) near6 simulat\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/09/13 17:45